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**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**In re Application of:**

Dennison et al.

**Serial No.:** Not Yet Assigned

**Filed:** December 12, 2001

**For:** OPTIMIZED CONTAINER STACKED  
CAPACITOR DRAM CELL UTILIZING  
SACRIFICIAL OXIDE DEPOSITION AND  
CHEMICAL MECHANICAL POLISHING

**Examiner:** Unknown

**Group Art Unit:** Unknown

**Attorney Docket No.:** 3259.1US (91-473.02  
RE)

jc826 U.S. PTO  
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**NOTICE OF EXPRESS MAILING**

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**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
Washington, D.C. 20231

Sir:

The present application is a continuation of reissue application Serial No. 08/759,058 filed October 7, 1996; which is a reissue application of U.S. Patent No. 5,270,241 issued December 14, 1993.

Pursuant to M.P.E.P. 2001.06(b), the Examiner is respectfully requested to consider the information of record in the prior applications, and to confirm in the first Office Action on the merits that such art has in fact been reviewed. A PTO-1449 form listing all of the information of record in the prior applications is enclosed herewith.

This Information Disclosure Statement is filed within three (3) months of the filing date of the above-identified application, and no certification pursuant to 37 C.F.R. § 1.97(c) or a fee pursuant to 37 C.F.R. 1.17(p) is required.

Respectfully submitted,



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Date: December 12, 2001

ERC/ps:le

Enclosures: Form PTO-1449  
Copy of documents cited

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